

## Andino X1 dual RS232 – Datasheet

	Raspberry 4:	Raspberry CM4 with Andino CM4 Adapter	
<b>SoC</b>	Broadcom BCM2711, Quad core Cortex-A72 (ARM v8) 64-bit SoC @ 1.5GHz	Broadcom BCM2837B0, Cortex-A53 (ARMv8) 64-bit SoC @ 1.4GHz	
<b>RAM</b>	4GB LPDDR4-3200 SDRAM	4GB LPDDR2 SDRAM	
<b>Flash</b>	None	32GB eMMC Flash	
<b>PCIe</b>	None	Interla PCIe X1 Port. Can be used for M.2 SSD via Adapter	
<b>WiFi</b>	2.4 GHz and 5.0 GHz IEEE 802.11ac wireless	None	
<b>Bluetooth</b>	Bluetooth 5.0, BLE	None	
<b>Ethernet</b>	Gigabit Ethernet	Gigabit Ethernet over USB 2.0 (max. throughput 300 Mbps)	
<b>Connectivity</b>	2 USB 3.0 ports; 2 USB 2.0 ports. Raspberry Pi standard 40 pin GPIO header 2 × micro-HDMI ports (up to 4kp60) Micro-SD card slot (accessible from outside Andino housing)	2 USB 2.0 ports external 1USB 2.0 port internal Full-size HDMI Extended 40-pin GPIO header	
<b>Delivery</b>	Andino IO, RaspberryPi 4, Breadboard, DIN rail Housing	Andino IO, CM4, Breadboard, DIN rail Housing	
<b>all variants</b>			
<b>Power Supply</b>	Wide range DC input 9-24V, Out: 5V, 2.6A	<b>Microcontroller type</b>	Atmel Atmega 328P (programming over Micro-USB)
<b>I/O's</b>	2 galvanic Isolation Inputs (isolated up to 5kV)    2 Relay Outputs (max. 24V, 1 A)	<b>RTC</b>	Integrated, battery-buffered Real Time Clock, DS3231 Dallas Semiconductors Accuracy: ± 2ppm between 0 °C and +40 °C
<b>EMC</b>	DIN EN 61000-6-2/3	<b>Abmessungen (H x B x T)</b>	115 mm X 108 mm x 60 mm
<b>Gehäusevariante</b>	Hutschienengehäuse (Kunststoff)		

See all Information about Andino X1 under [Andino X1 - Overview & Datasheet.pdf](#) | [Andino X1 - Industrial Raspberry Pi PC](#) | [Andino](#)

EMC -Report <https://andino.systems/andino-x1/emc/ANDINO-X1-EMC-Report.pdf>

ROHS – Report <https://andino.systems/andino-x1/emc/ROHS-X1.pdf>

REACH-Report <https://andino.systems/andino-x1/emc/REACH-Clear%20Systems.pdf>

	NXP SC16IS752
<b>Power supply</b>	3.3V
<b>UART</b>	Dual full duplex UART
<b>Temperature</b>	-40°C to +95°C
<b>Interface</b>	I2C/SPI Bus selectable
<b>FIFO</b>	64 Bytes FIFO (transmitter and receiver)
<b>Baud rate</b>	Baud rates up to 5 Mbit/s
<b>Features</b>	Auto hardware flow control using RTS/CTS Fully programmable character formatting (5 bit, 6 bit, 7 bit or 8-bit character) Line break generation and detection 5V tolerant inputs

See all Information about NXP SC16IS752 under [https://www.nxp.com/docs/en/data-sheet/SC16IS752\\_SC16IS762.pdf](https://www.nxp.com/docs/en/data-sheet/SC16IS752_SC16IS762.pdf)